

# PATENT ABSTRACTS OF JAPAN

(11)Publication number : 10-333992

(43)Date of publication of application : 18.12.1998

(51)Int.CI.

G06F 12/16

B41J 29/38

G11C 16/02

(21)Application number : 09-153090

(71)Applicant : CANON INC

(22)Date of filing : 28.05.1997

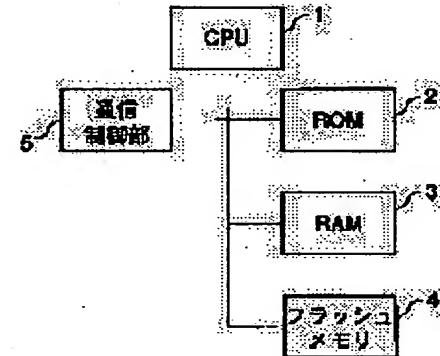
(72)Inventor : SHIMAMURA YOSHIYUKI

## (54) FLASH MEMORY WRITE DATA AND FLASH MEMORY LOADING EQUIPMENT

### (57)Abstract:

**PROBLEM TO BE SOLVED:** To surely detect the write error of a flash memory, data destruction and the write of an unsuitable program by setting sum value adjustment data for turning the sum value of data to a specified value inside the flash memory.

**SOLUTION:** When the power of this flash memory loading equipment is turned ON, a CPU 1 calculates the sum value of the flash memory 4, discriminates whether or not the obtained sum value and the specified value intrinsic to the flash memory loading equipment provided inside a ROM 2 match, and when the sum value and the specified value do not match, since the flash memory 4 is abnormal or the program unsuitable for the flash memory loading equipment is present inside the flash memory 4, transmits the write request signals of a host device flash memory 4 through a communication control part 5. Then, in the case that flash memory write data are sent from a host device, the received flash memory write data are written to the flash memory 4.



### LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office